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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group: Unknown

Attorney

Docket. 5727-65832

Applicant. Zindel Herbert Heller

Invention. INSTRUMENT

Serial No: 10/046,030

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Examiner: Unknown

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Assistant Commissioner for Patents, Washington, D.C. 20231

on February 5, 2002

Kim Tyree
Kim Tyree

Dated: February 5, 2002

LETTER TO OFFICIAL DRAFTSMAN
SUBMISSION OF ACCEPTABLE DRAWINGS

Commissioner for Patents
Washington, D.C. 20231

Sir:

Applicant submits herewith seven sheets of acceptable drawings (Figs. 1-11) for the above-identified application.

Accordingly, Applicant submits that this application is condition for grant. Such action is respectfully requested.

Respectfully submitted,

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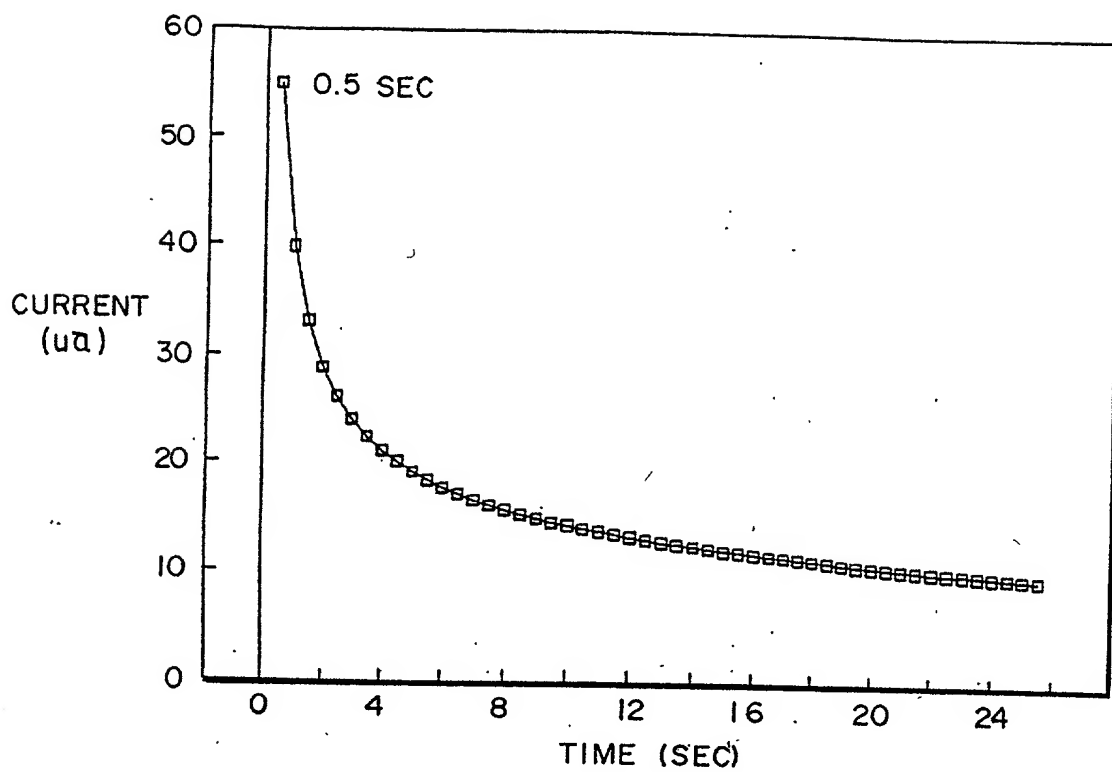


FIG. 1

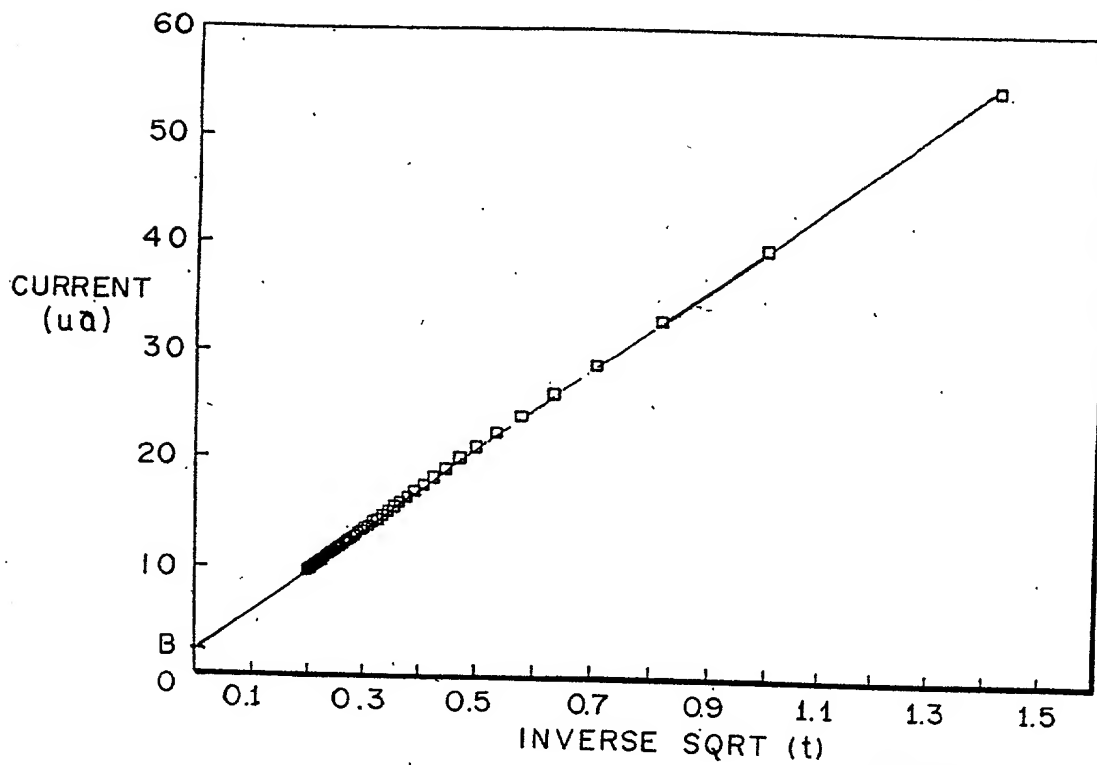


FIG. 2

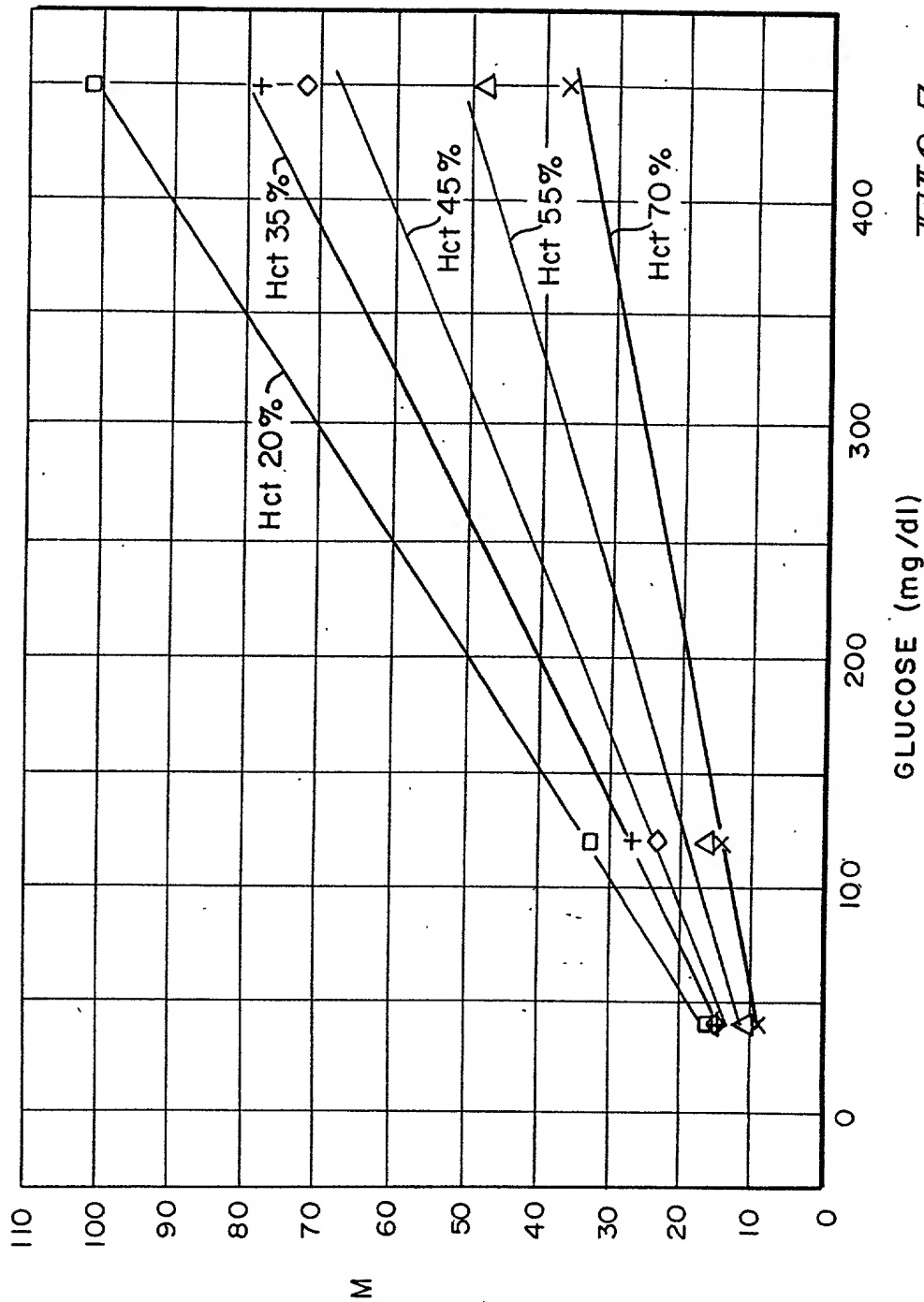


FIG 3

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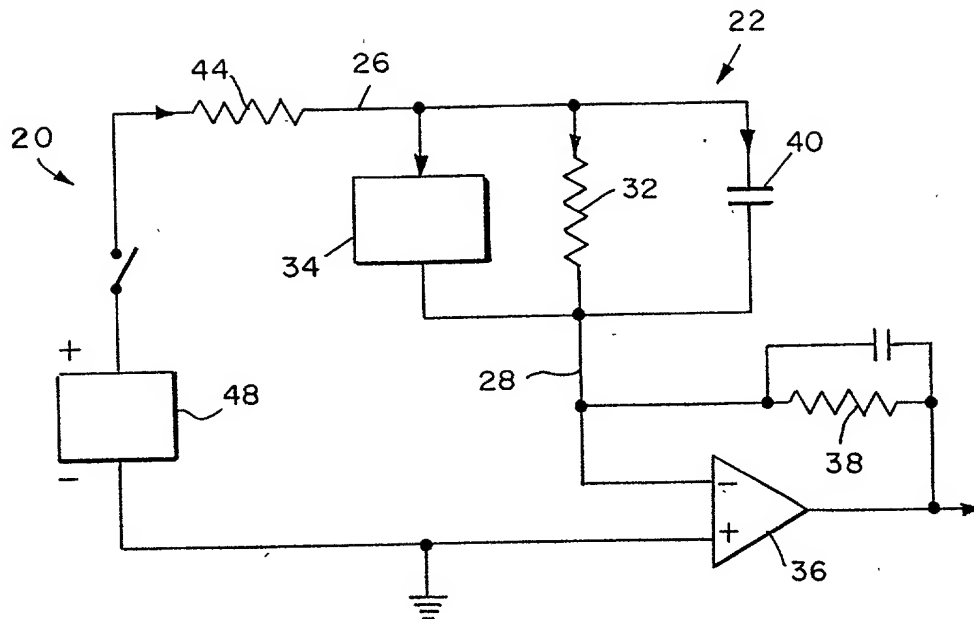


FIG. 4

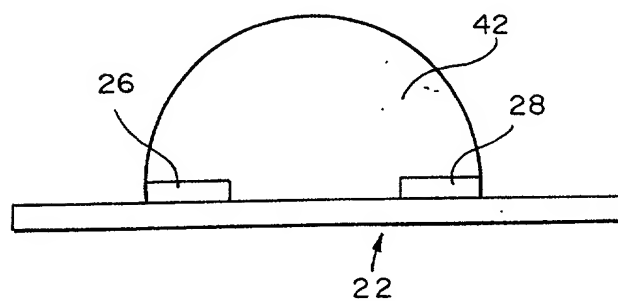


FIG. 5

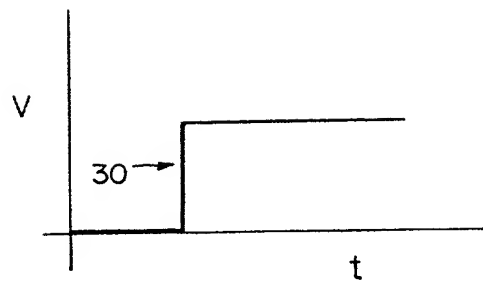


FIG. 6

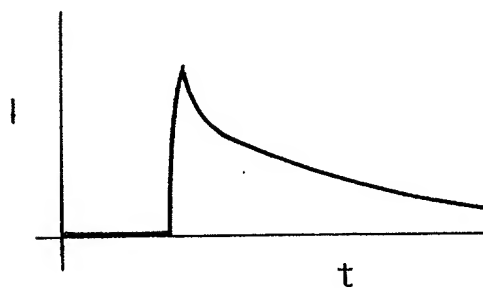


FIG. 7

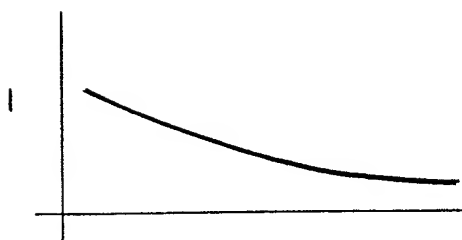


FIG. 8

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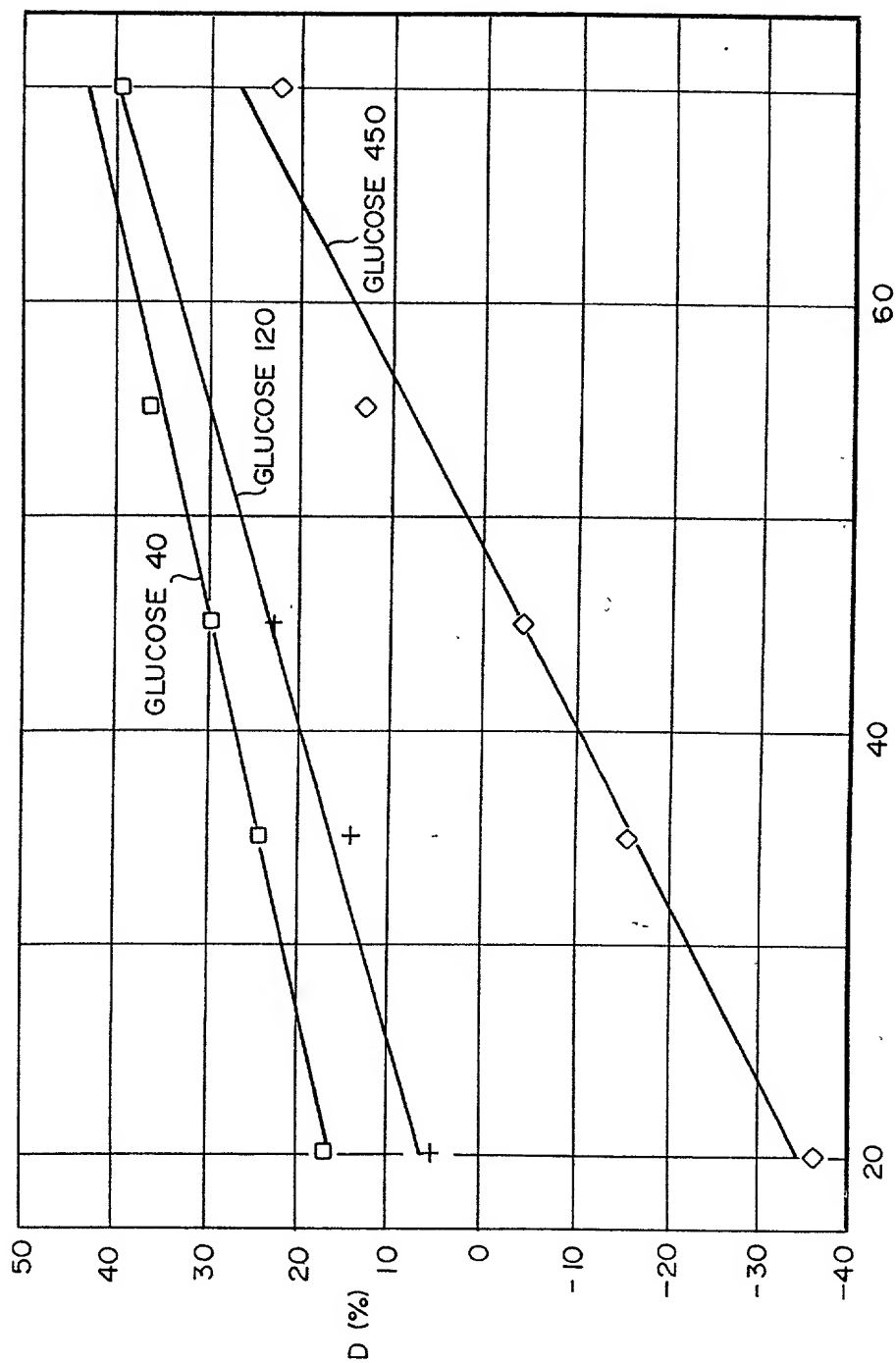


FIG. 9

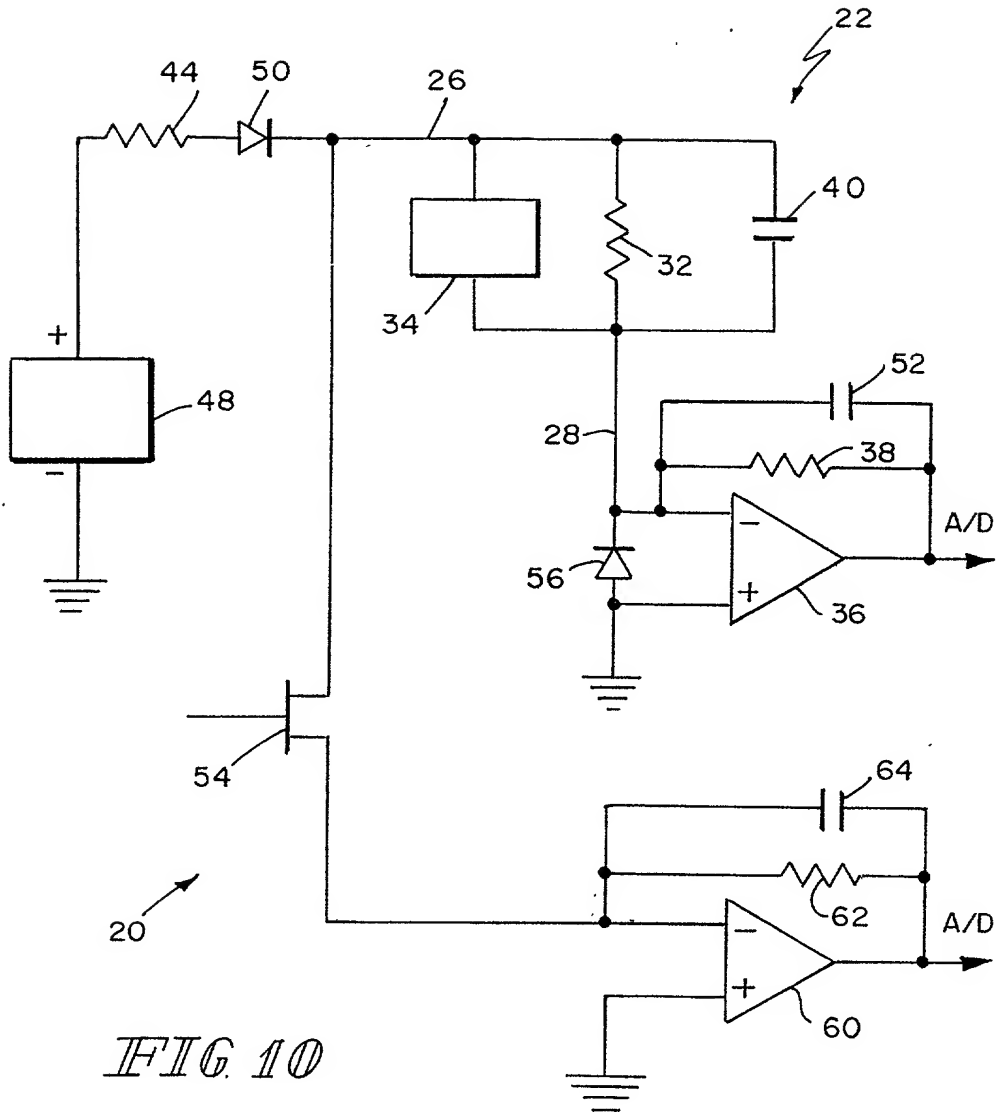


FIG 10

The circuit diagram illustrates a differential amplifier system for measuring the resistance of a variable resistor 26. A voltage source 48 is connected in series with a resistor 44 and a diode 50. The output of this series combination is connected to one terminal of the variable resistor 26. The other terminal of the variable resistor 26 is connected to a fixed resistor 72, which is in series with a battery 74. The output of this branch is connected to the non-inverting input (+) of an operational amplifier 36. The inverting input (-) of op-amp 36 is connected to a diode 56, which is in series with a resistor 38. The output of op-amp 36 is connected to an A/D converter. The other terminal of the variable resistor 26 is connected to the emitter of a transistor 54. The base of transistor 54 is connected to a fixed resistor 62, which is in series with a battery 64. The output of this branch is connected to the non-inverting input (+) of an operational amplifier 60. The inverting input (-) of op-amp 60 is connected to a diode 56, which is in series with a resistor 62. The output of op-amp 60 is connected to an A/D converter. The circuit is powered by a voltage source 48 and a battery 74. The variable resistor 26 is connected to the output of the first op-amp 36 and the emitter of the transistor 54. The circuit is labeled with reference numerals 20, 22, 26, 44, 48, 50, 52, 54, 56, 60, 62, 64, 70, 72, and 74.

FIG. 11